

# Graduate Engineer | Čačak

This is a superb new opportunity for an enthusiastic graduate (or almost graduate) to join a rapidly growing team of professionals.

This role is suitable for a talented fresh graduate, or engineer with some prior experience in the industry with a strong background in digital ASIC / SoC design looking for a significant challenge.

Join us for a career of a lifetime, and take part in one of the industry's most forward thinking projects.

Come work with the best team of software and hardware engineers.



## Skills

- Graduate (or almost graduate) in B.Sc. of Electrical Engineering or computer engineering. M.Sc - an advantage.
- Major in VLSI, Computers Science.
- Understanding of computer architecture and micro-architecture concepts.
- Familiarity with hardware description languages, such as VHDL or Verilog / SystemVerilog.
- Knowledge of object oriented programming - an advantage
- Familiarity with at least one of the programming languages: System Verilog, Verilog, C, Perl, Python, C++
- Knowledge of the UNIX environment and shell programming/scripting - An advantage
- Strong communication and interpersonal skills
- Fluent in English

Sounds like you? Send your CV to [jobs@thevtool.com](mailto:jobs@thevtool.com)